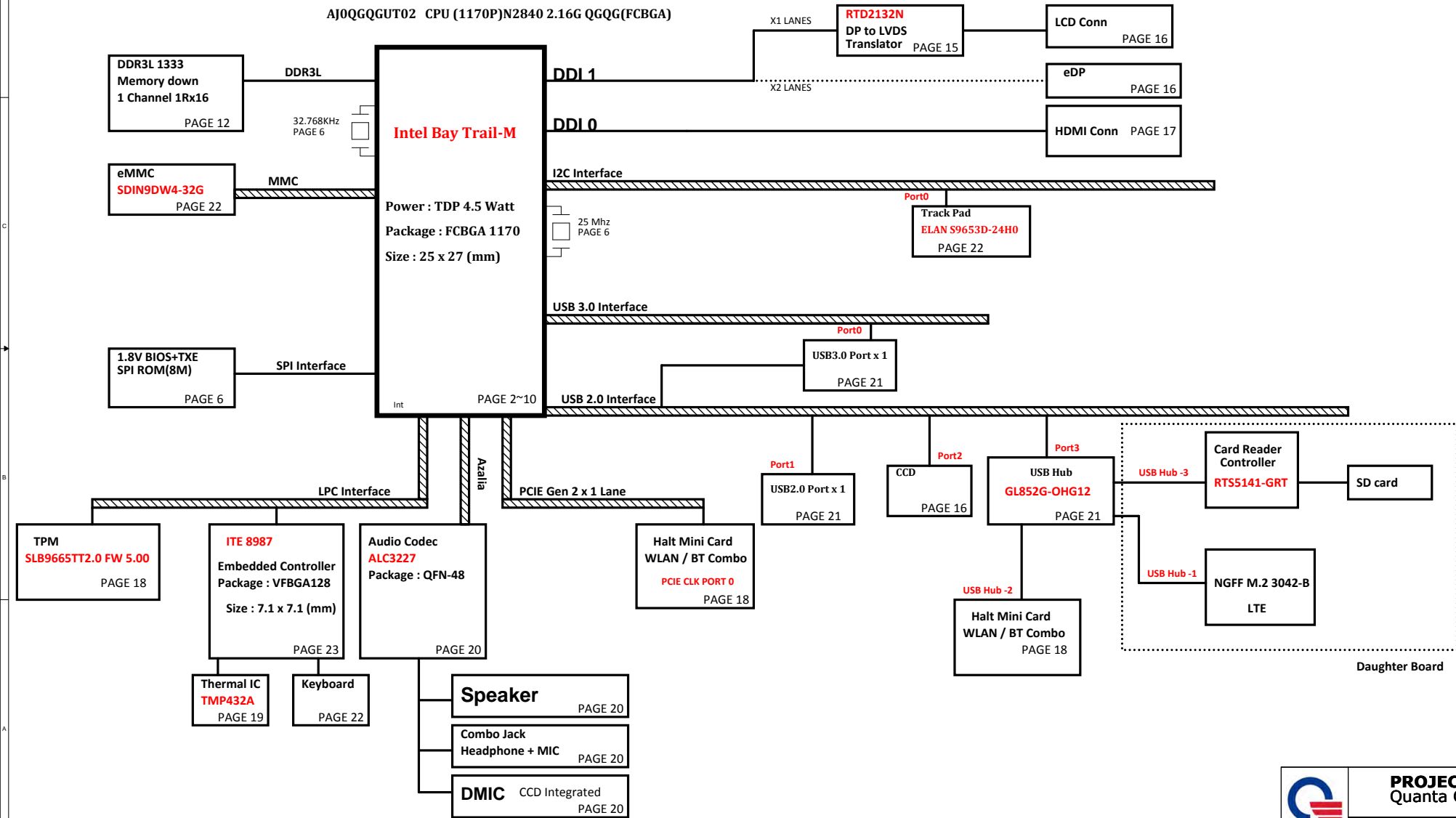


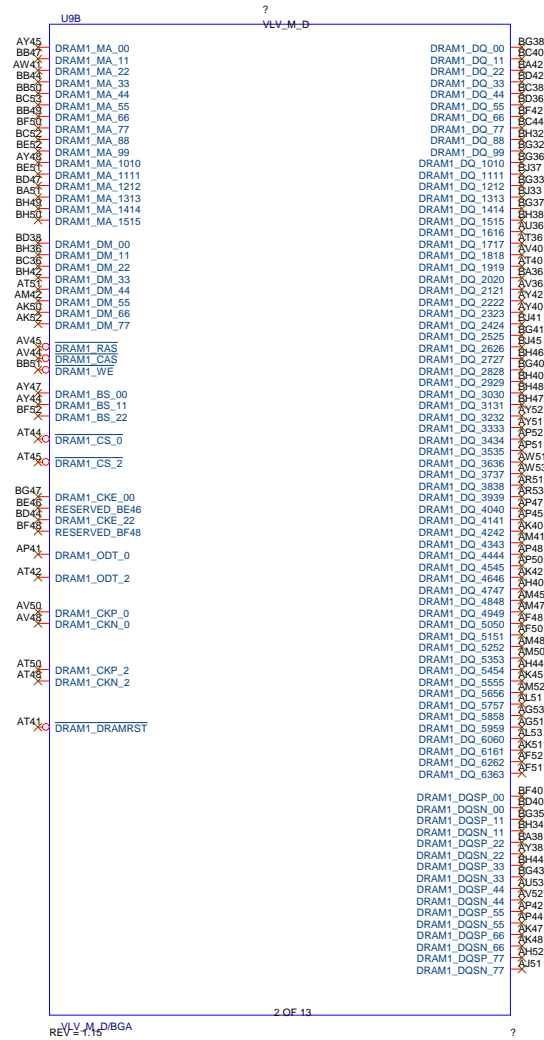
# Y0A Intel Bay Trail-M Platform Block Diagram

AJ0QGQLUT02 CPU(1170P)N2940 1.83G QGQL(FCBGA)

AJ0QGQGUT02 CPU (1170P)N2840 2.16G QGQG(FCBGA)



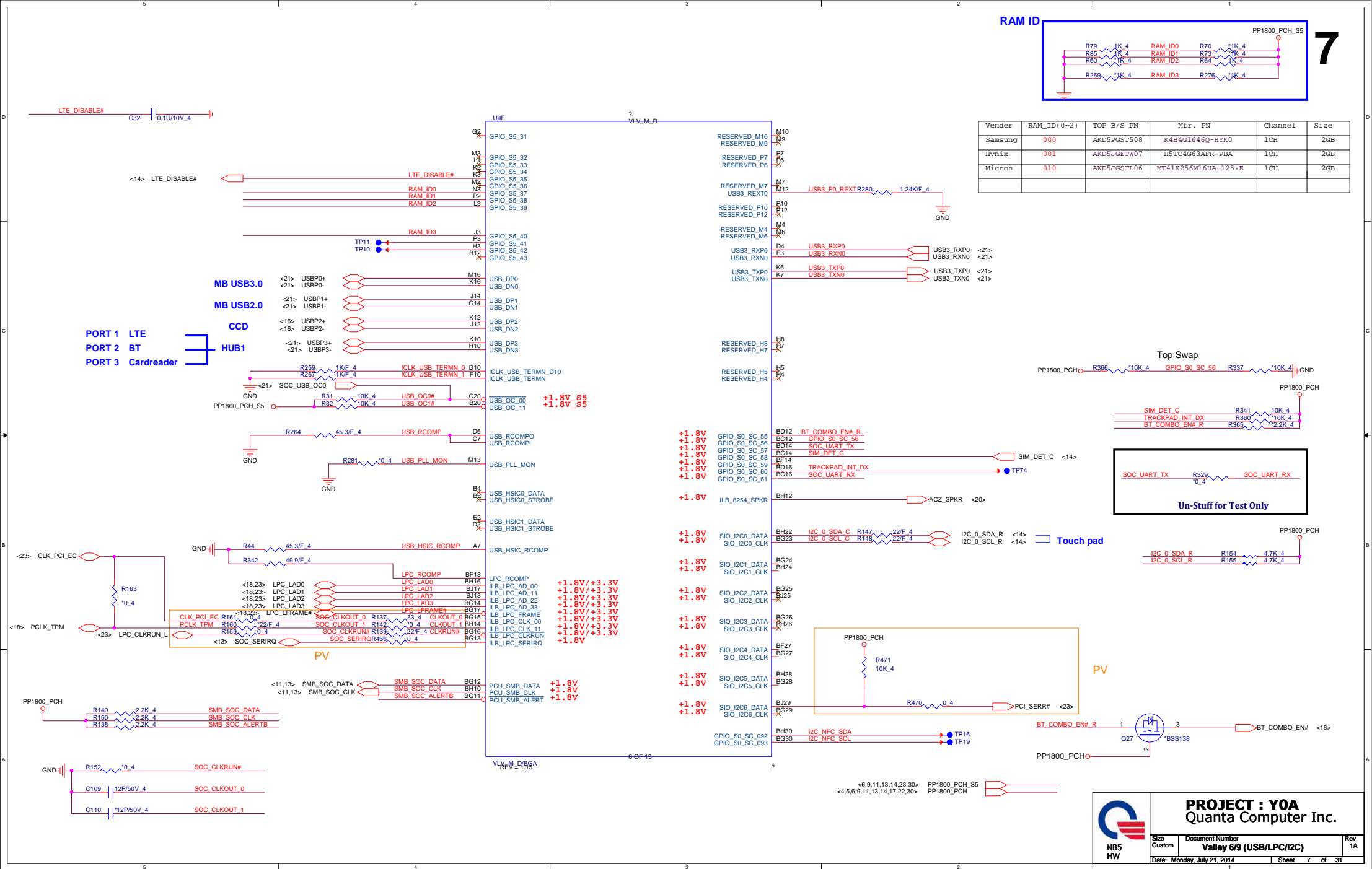








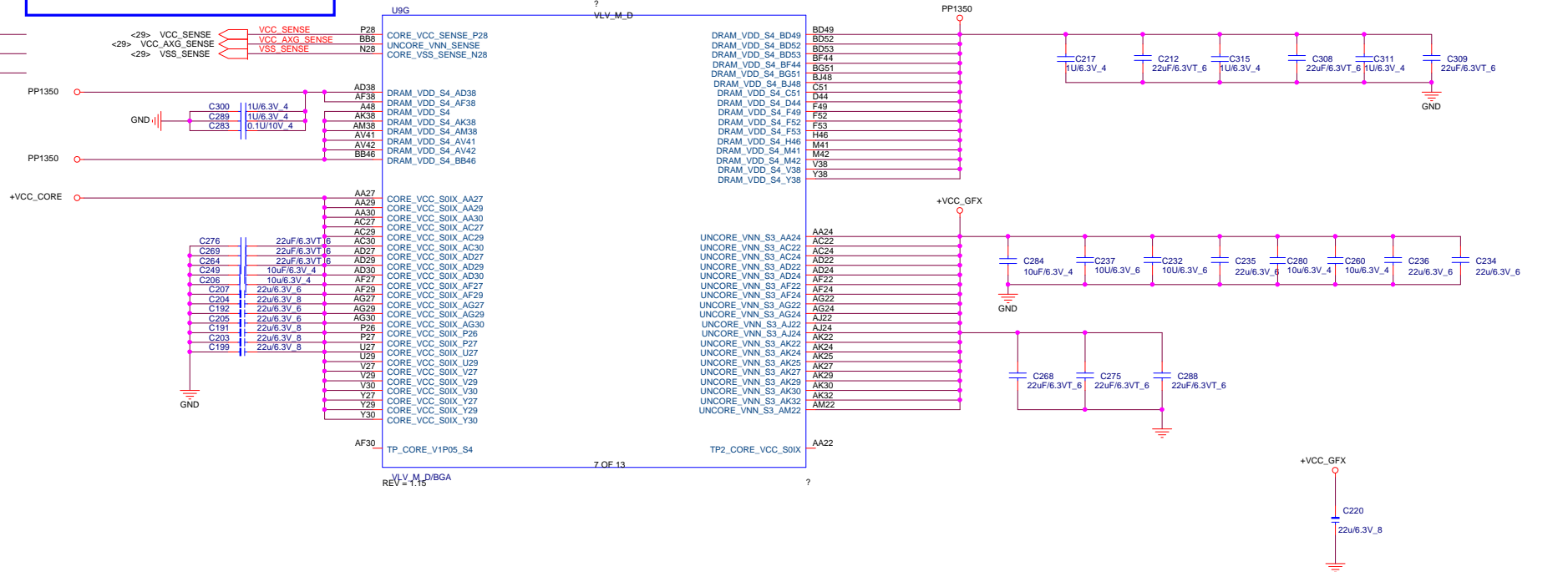




layout suggestion by intel,  
VSS\_AXG\_SENSE didn't  
connect to VSS\_SENSE, will  
connect the GND via near  
VCC\_AXG\_SENSE

for layout, add 0hm between  
GND and VSS\_AXG\_SENSE

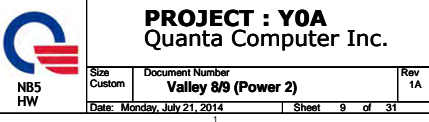
<29> VSS\_AXG\_SENSE



<29> +VCC\_GFX  
<29> +VCC\_CORE  
<2,12,27,30> PP1350

	<b>PROJECT : Y0A</b> <b>Quanta Computer Inc.</b>		
	Size Custom	Document Number <b>Valley 7/9 (Power 1)</b>	Rev 1A
	Date: Monday, July 21, 2014	Sheet 8 of 31	1



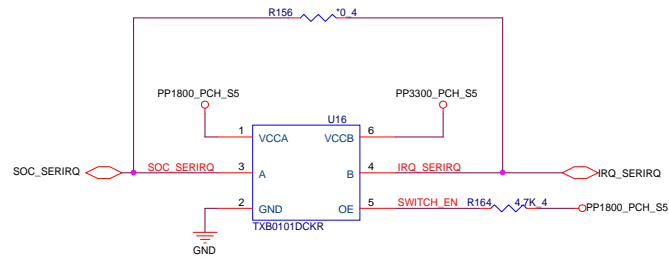




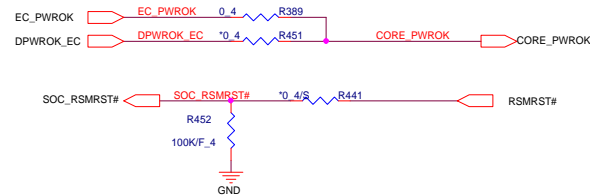
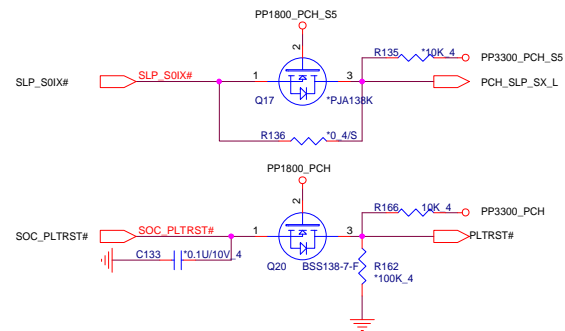
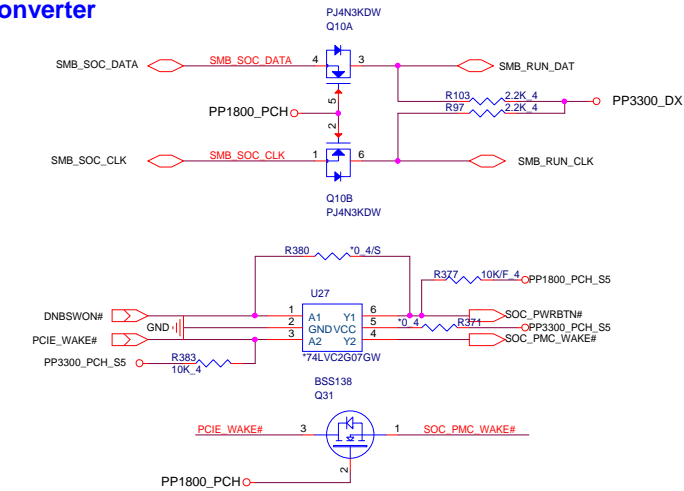
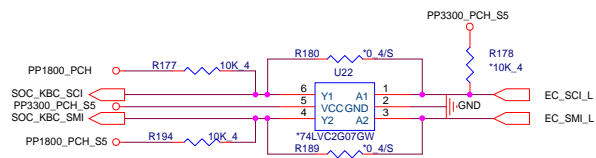
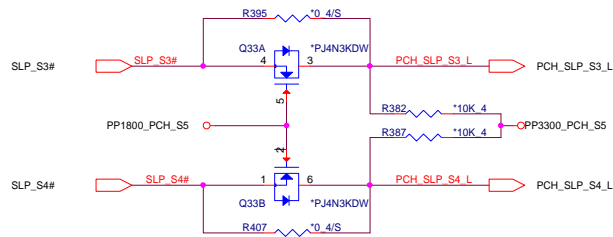
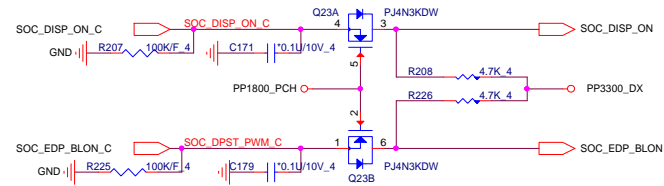




## eDP control pin (For eDP)

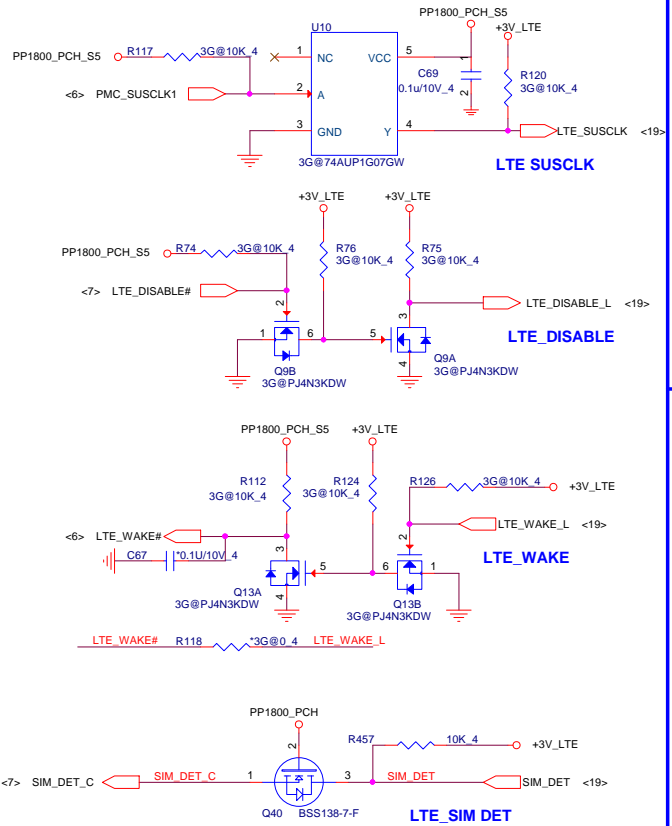


## LVDS converter

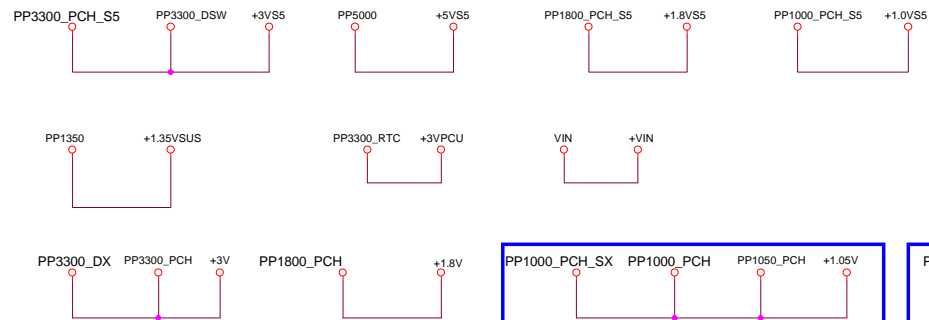
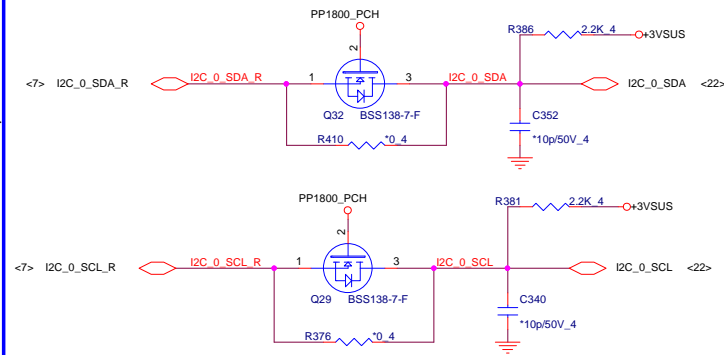


PP1800\_PCH\_S5  
PP3300\_PCH\_S5  
PP1800\_PCH  
PP3300\_PCH  
PP3300\_DX

## LTE



## Track Pad

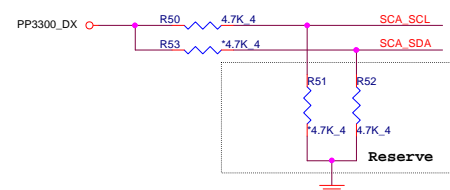
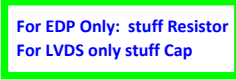


Combine +1.0VSX,+1.0V,+1.05V  
power rail for cost saving

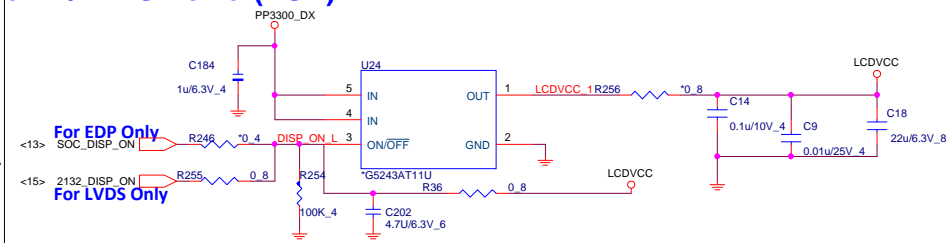
Combine +1.35VSFR,+1.35V  
power rail for cost saving

<6,7,9,11,13,28,30> PP1800\_PCH\_S5  
<19> +3V\_LTE  
<4,5,6,7,9,11,13,17,22,30> PP1800\_PCH  
<22,27,30> +3VSUS  
<2,8,12,27,30> PP1350

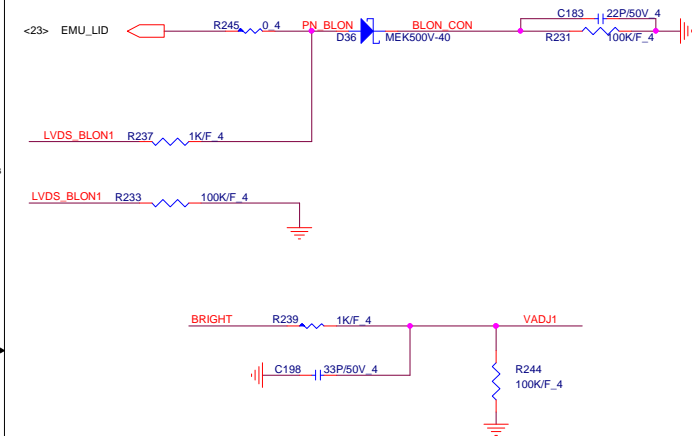
	<b>PROJECT : Y0A</b>		
	Quanta Computer Inc.		
	Size Custom	Document Number	Rev 1A
	<b>Level Shifter (SOC_DEV)</b>		
	Date: Monday, July 21, 2014	Sheet 14 of 31	



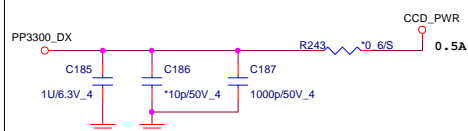
eDP /LVDS Power(VGA)



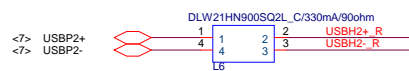
## LID Switch



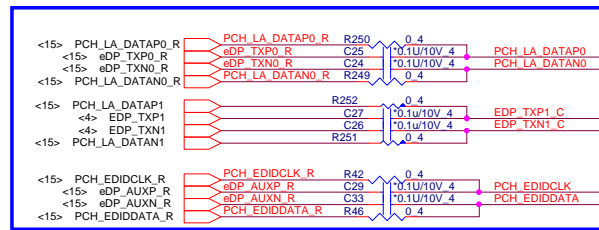
**CCD power(CCD)**



## CCD USB(CCD)

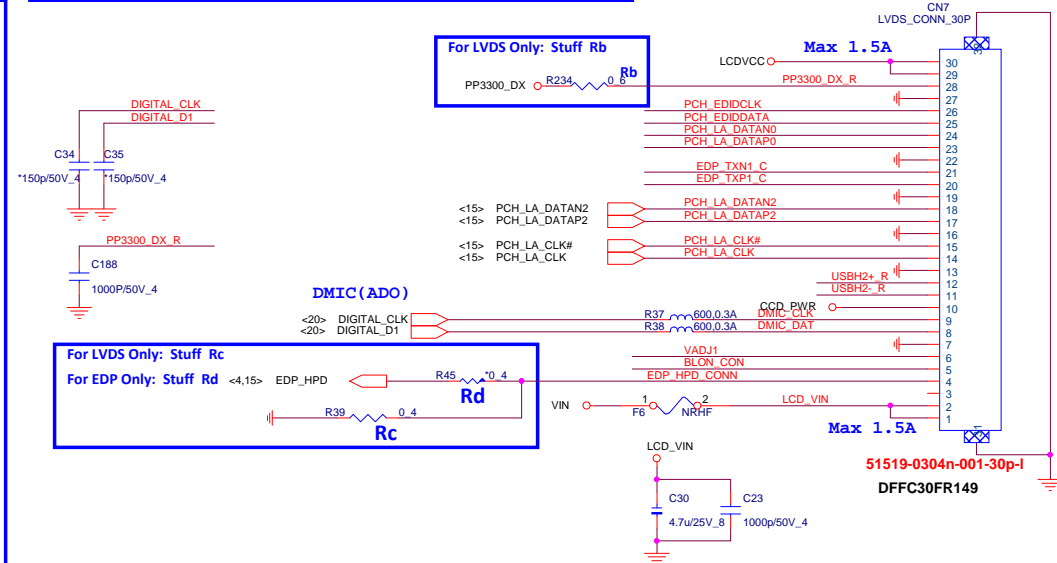


For LVDS Only stuff R, For EDP only stuff C

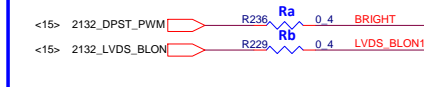


## eDP/LVDS

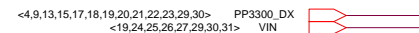
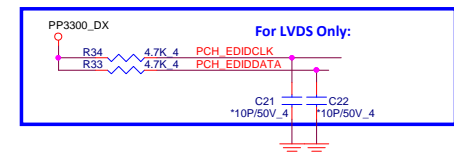
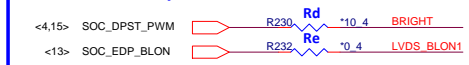
## Need New LCD Cable



For LVDS Only: stuff Ra,Rb



For EDP Only: stuff Rd,Re



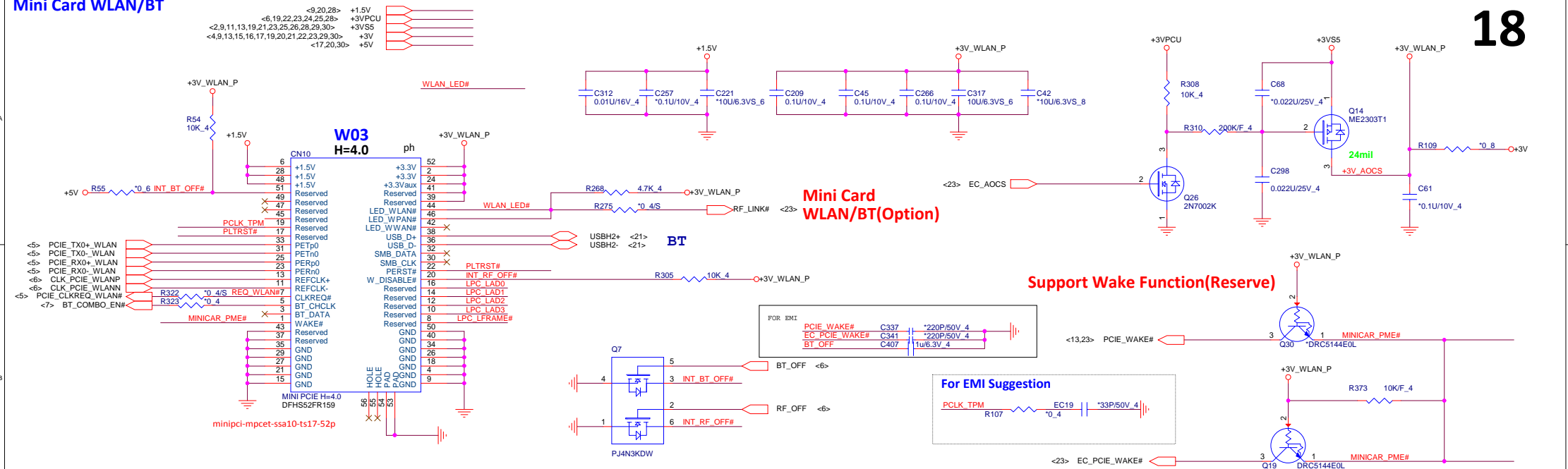
**PROJECT : YOA**  
Quanta Computer Inc.

Size Custom	Document Number <b>LVDS/eDP/CCD/DMIC</b>	Rev 1A
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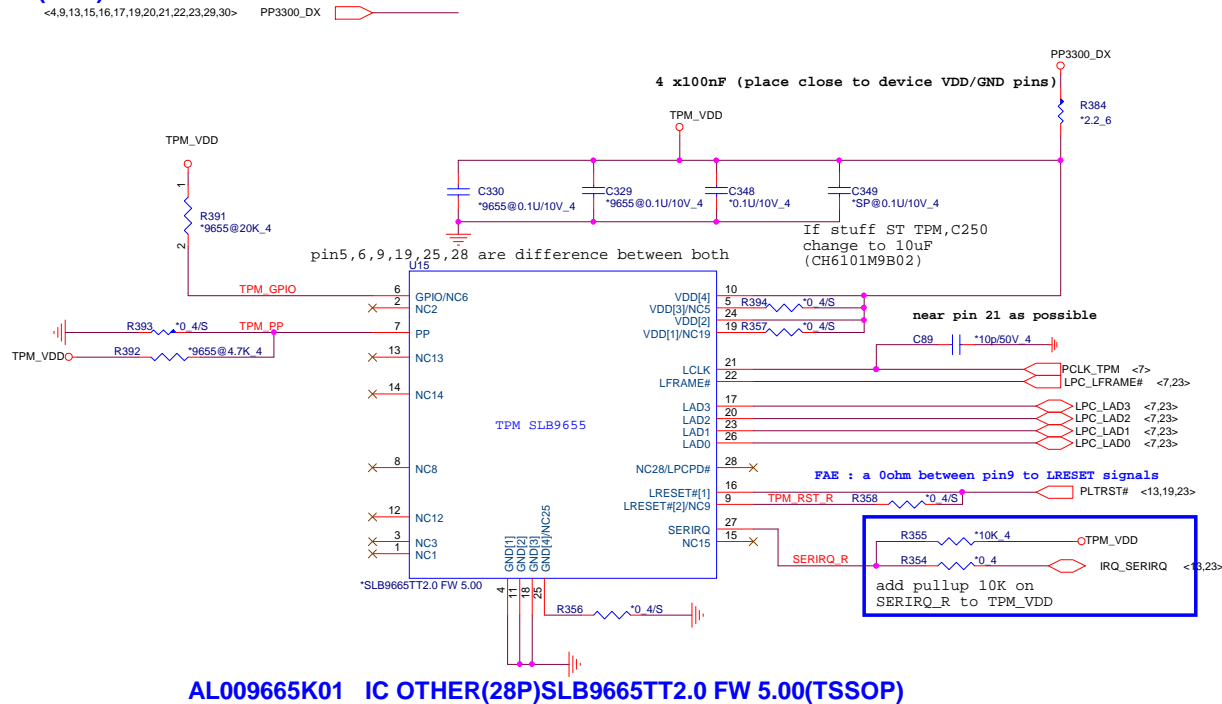




## Mini Card WLAN/BT

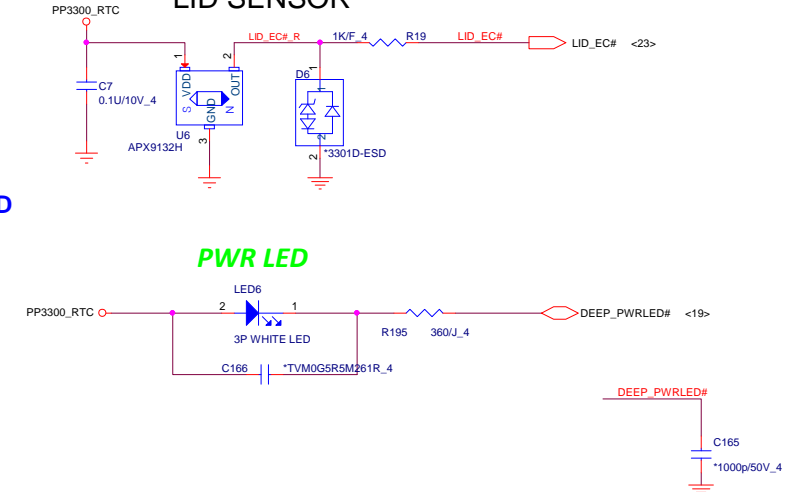


## TPM (CLG)



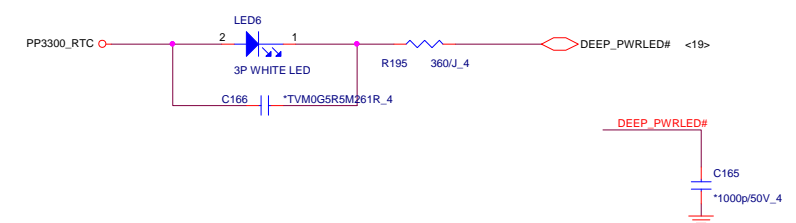
## LID

## LID SENSOR



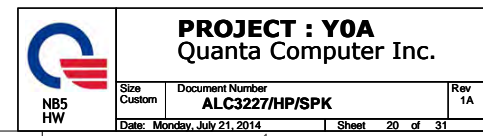
## LED

## PWR LED

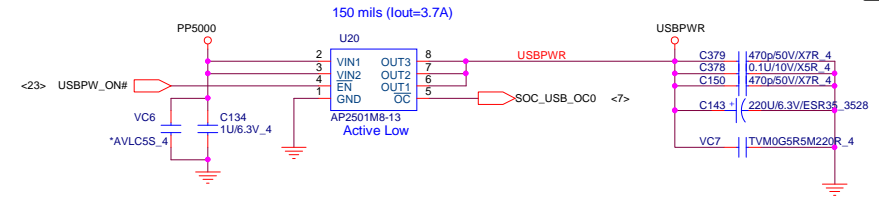


	<b>PROJECT : Y0A</b>		
	Quanta Computer Inc.		
	Size Custom	Document Number	Rev 1A
	<b>WLAN/TPM/LED/LID/SW</b>		
	Date: Monday, July 21, 2014	Sheet	18 of 31

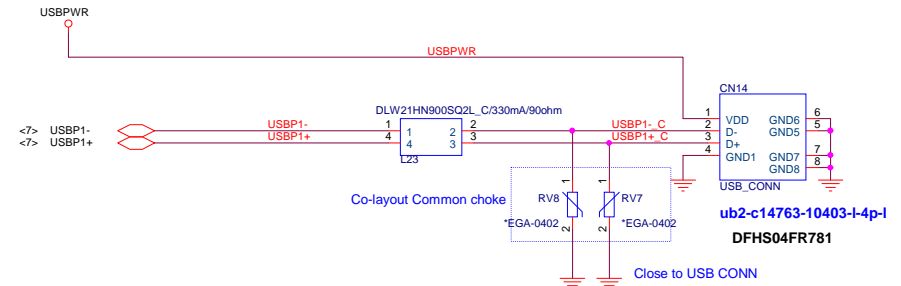




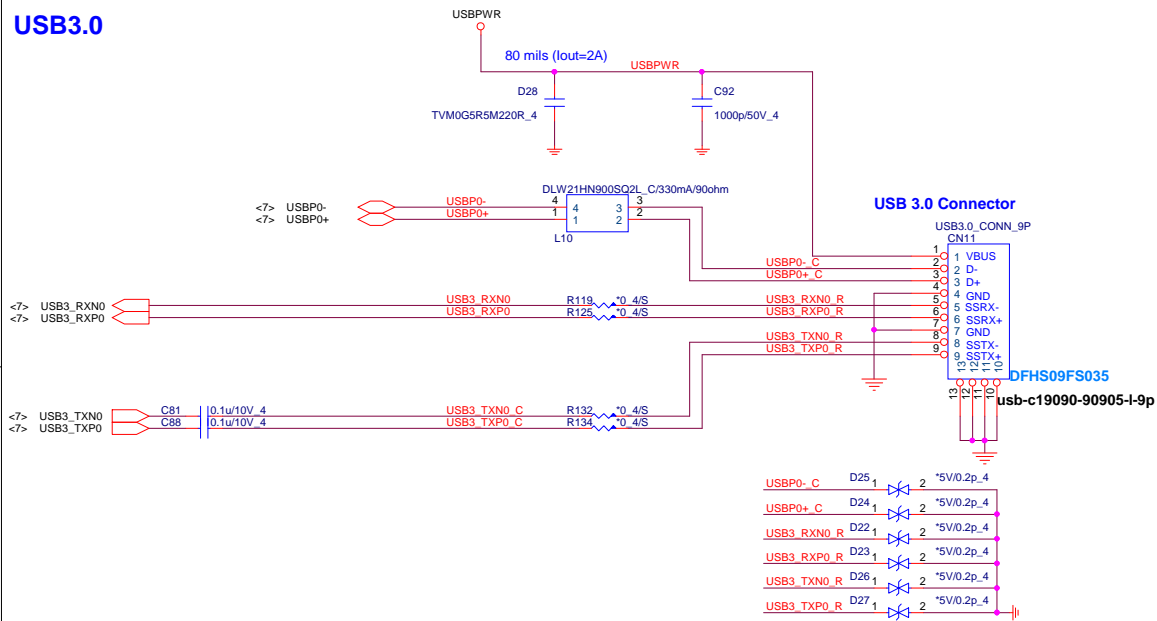
## USB POWER SWITCH



## USB 2.0

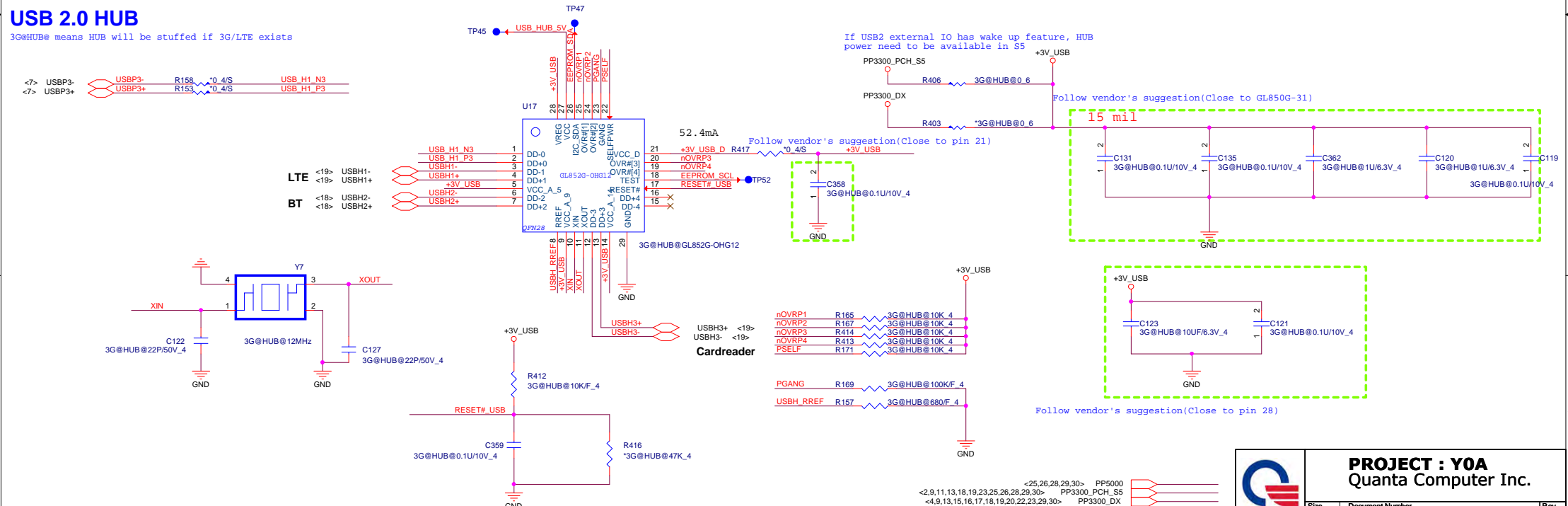


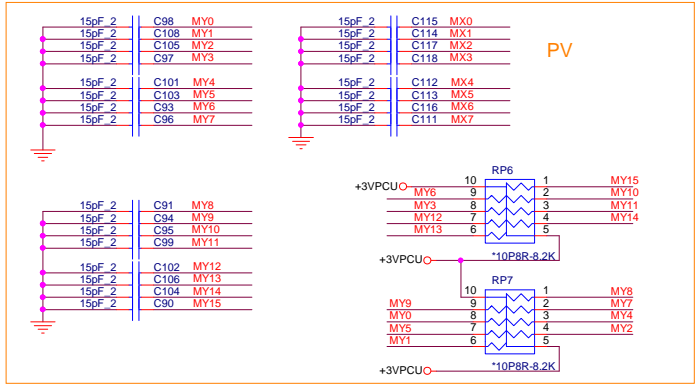
## USB3.0



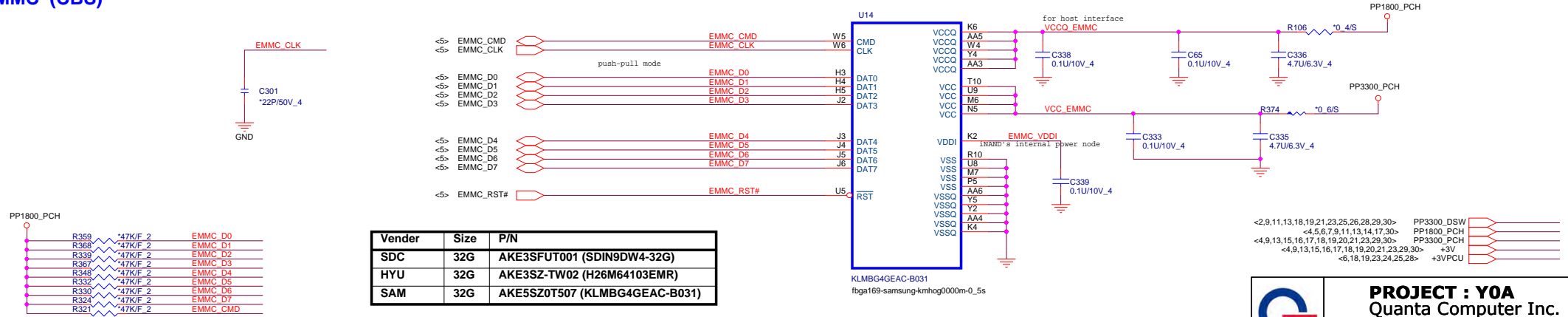
## USB 2.0 HUB

3G@HUB@ means HUB will be stuffed if 3G/LTE exists






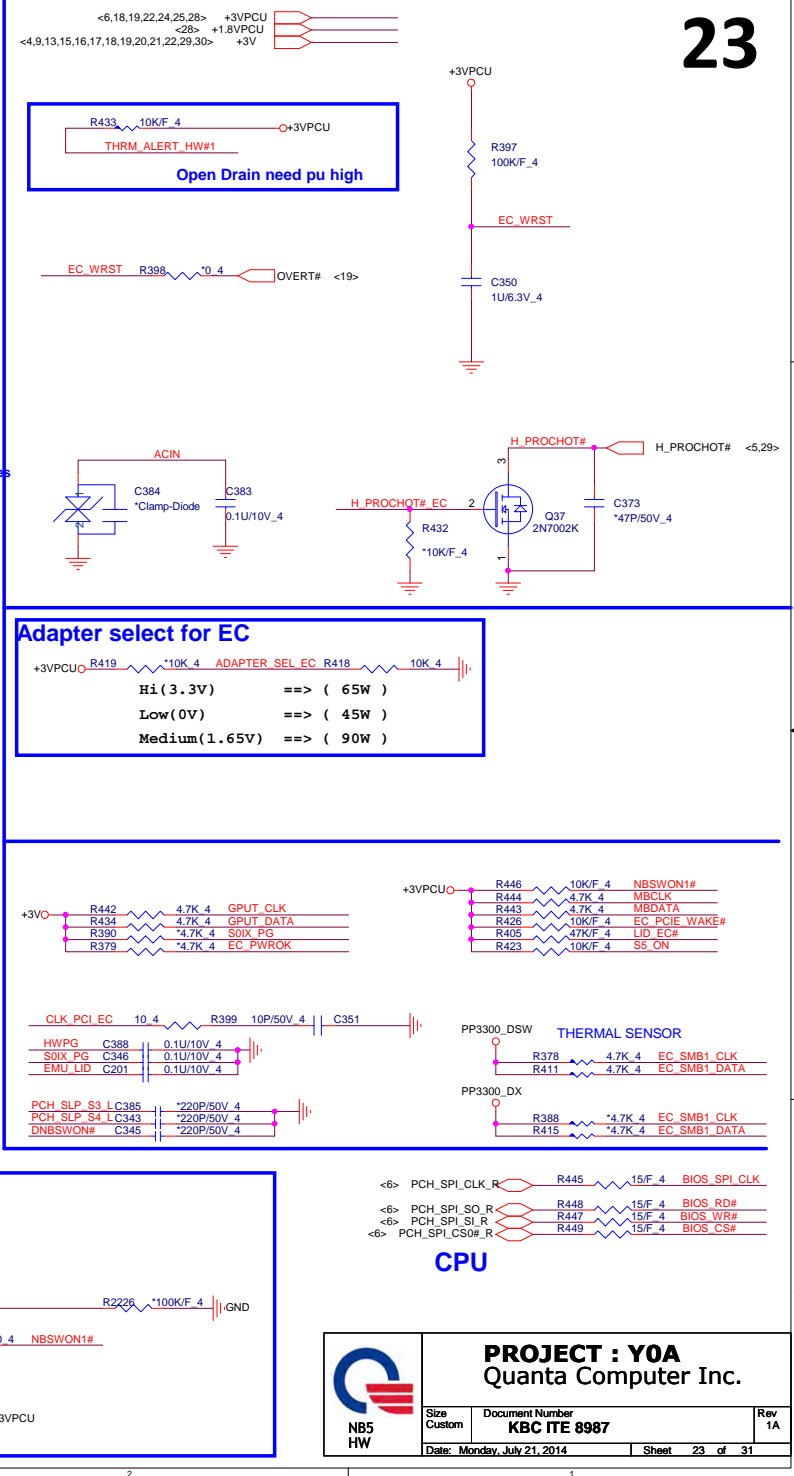
## EMMC (CBS)



Vender	Size	P/N
SDC	32G	AKE3SFUT001 (SDIN9DW4-32G)
HYU	32G	AKE3SZ-TW02 (H26M64103EMR)
SAM	32G	AKE5SZ0T507 (KLMBG4GEAC-B031)


 NB5 HW	<b>PROJECT : Y0A</b> Quanta Computer Inc.		
	Size Custom	Document Number <b>KB/TP/EMMC</b>	Rev 1A
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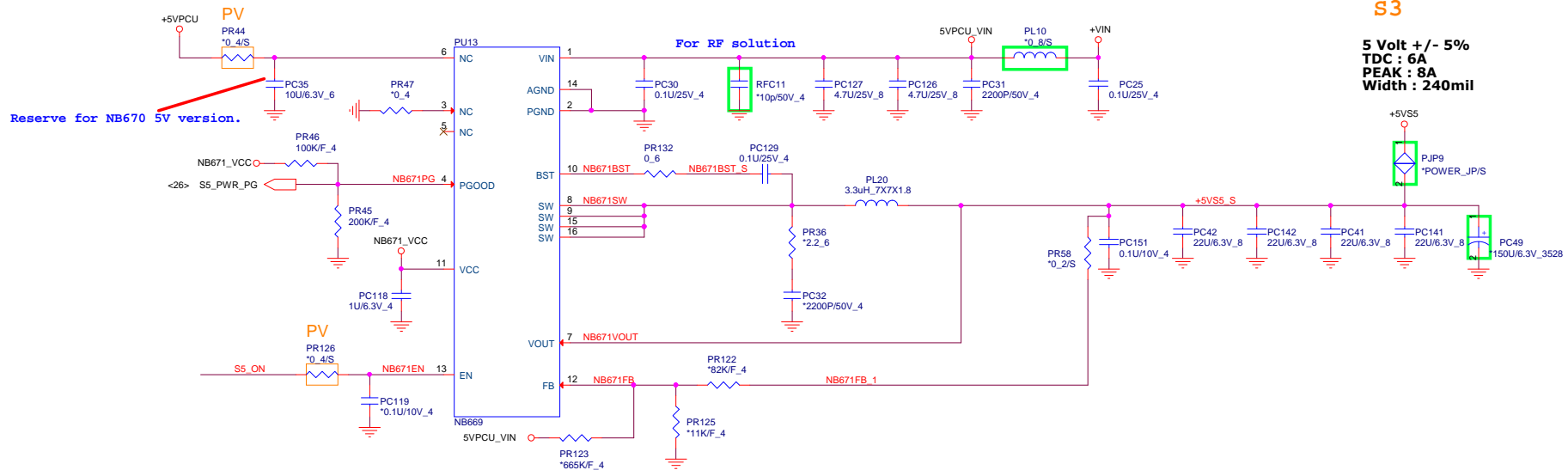
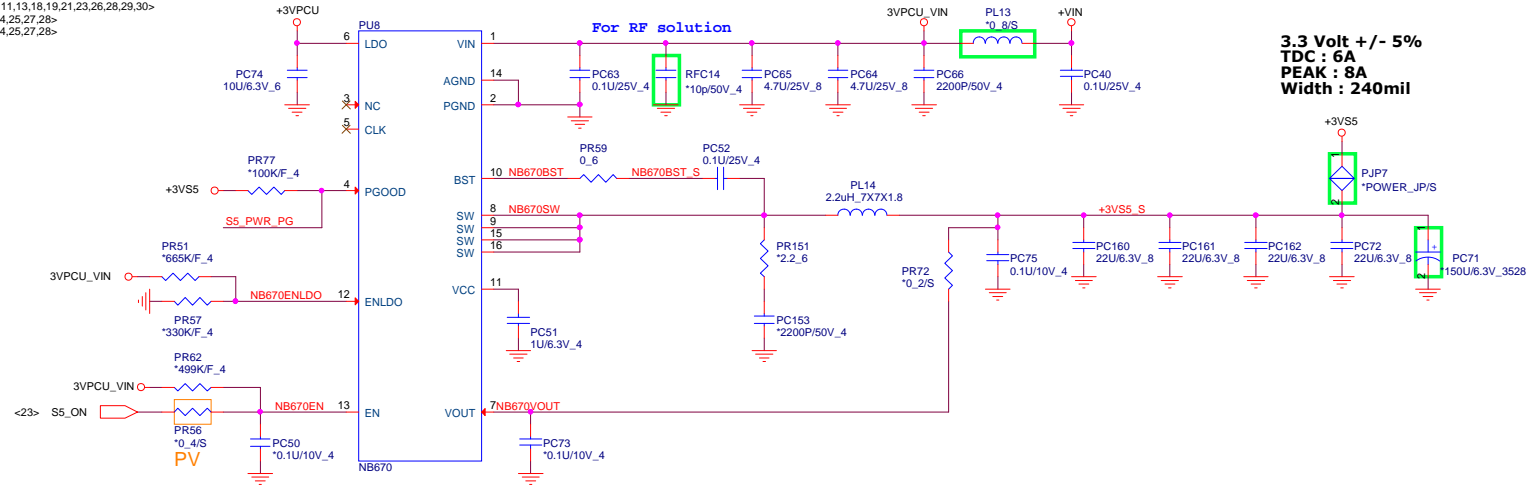


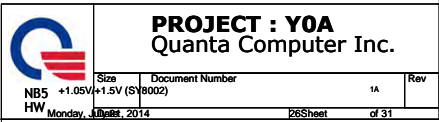
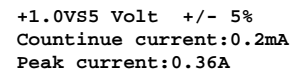
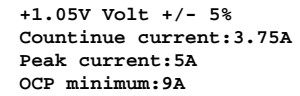
**45/65W DC\_JACK**

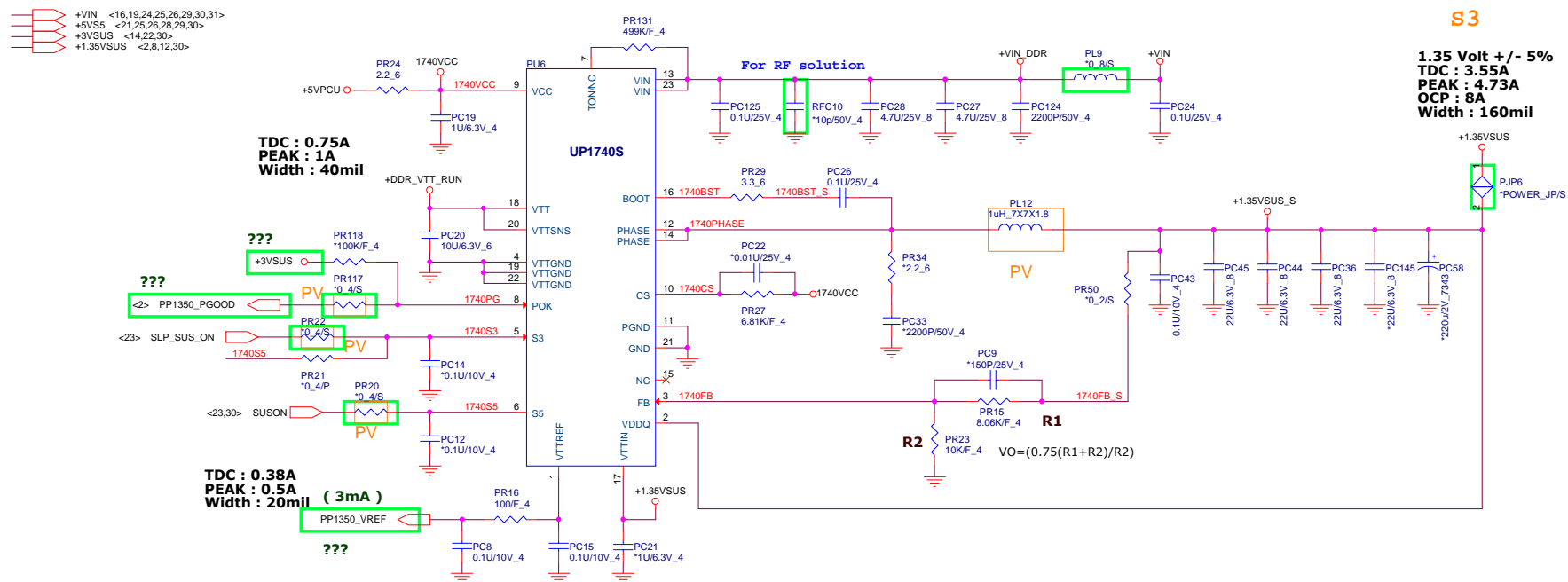
 NB5 HW	<b>PROJECT : Y0A</b> Quanta Computer Inc.		
	Size Custom	Document Number <b>Charger (OZ8691)</b>	Rev 1A
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+VIN <16,19,24,26,27,29,30,31>  
 +3VPCU <6,18,19,22,23,24,28>  
 +3VS5 <2,9,11,13,18,19,21,23,26,28,29,30>  
 +5VPCU <24,25,27,28>  
 +5VS5 <24,25,27,28>







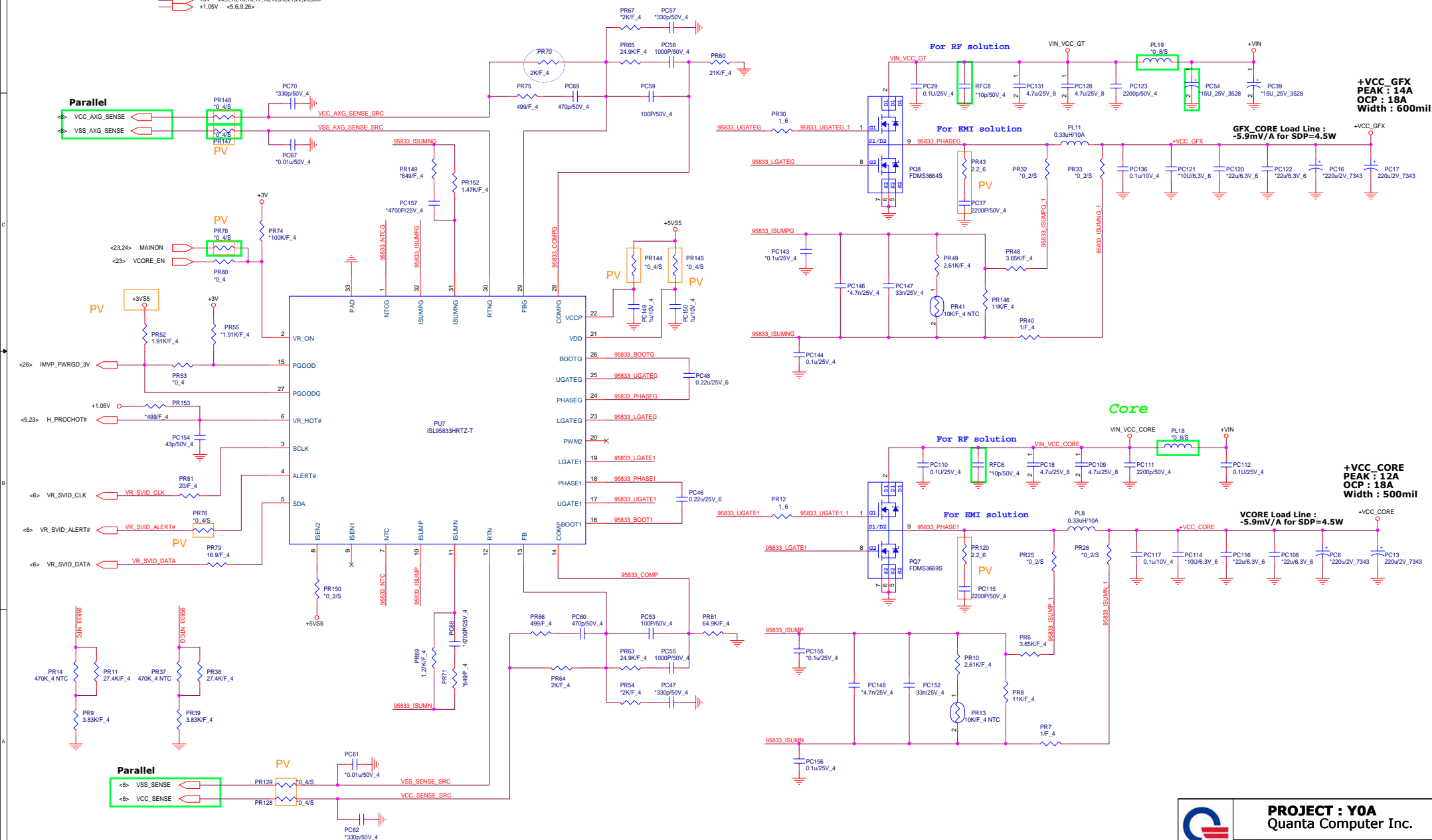
	S3	S5	+1.35VSUS	REF	VTT
S0	1	1	ON	ON	ON
S3 (main on off)	0	1	ON	ON	OFF
S4/S5	0	0	OFF	OFF	OFF

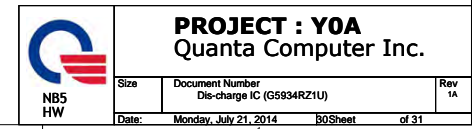


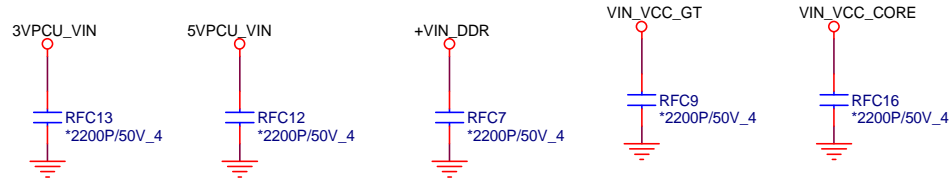
**PROJECT : Y0A**  
Quanta Computer Inc.

Size Custom Document Number **DDR 1.35V(UP1740S)** Rev 1A  
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## TOP



## BOTTOM

